

csp2hc: from CSP_M to **Handel-C**

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Contents

1	Introduction	5
2	Current Status	6
2.1	Restrictions Verification	13
2.2	Identified Errors in the Parser/Type Checker	14
2.3	Directives	15
2.3.1	Input and Output Channels	15
2.3.2	Argument type	16
2.3.3	Main Process	16
2.3.4	Number of bits for integers	16
2.3.5	Type of Empty Sets	17
3	Translating CMOS	17
3.1	Phase Controller	18
3.2	Heap Model	18
3.3	CMOS	26
4	Using <i>csp2hc</i>	29
5	Further requirements	31
6	Transcripts	32
6.1	Dining Philosophers	32
6.1.1	CSP_M	32
6.1.2	Handel-C	35
6.2	Level Crossing	46
6.2.1	CSP_M	46
6.2.2	Handel-C	47
6.3	CMOS Phase Controller	55
6.3.1	CSP_M	55
6.3.2	Handel-C	58

1 Introduction

Our tool, *csp2hc*, already mechanises the translation of a considerable subset of CSP_M to Handel-C, which includes the following features.

1. SKIP
2. STOP
3. Sequential composition
4. Recursion
5. Prefixing
6. External Choice
7. Concurrency
8. Datatypes
9. Constants
10. Expressions
11. `if _ then _ else _`

Although they represent a subset of CSP_M , using these constructors, we are already able to automatically translate many of the classical CSP examples in the literature, like the examples presented in Appendix ??: the dining philosophers and the level crossing. More importantly, the phase controller of the CMOS can already be automatically translated using *csp2hc*. Currently, we are working on the features needed to achieve the automatic translation of the heap model of the CMOS. Ultimately, we aim at the automatic translation of the whole CMOS.

In what follows we describe the details of our efforts. Section 2 discussed the aspects involving the current status of *csp2hc*. These include the conventions and assumptions made by *csp2hc* on the source CSP_M specification, a list of the CSP_M constructors that are supported by *csp2hc*, the current restrictions on these constructors, and an outline of the solutions implemented by the translator. *The vast majority of these restrictions are intended to be removed in the next stages of the project.* The solutions for these are presented as needed in Section 3.

In Section 2.1, we list which restrictions on the CSP_M are already being automatically checked by *csp2hc* and which of them are not being automatically checked by the tool.

Our tool uses a CSP_M parser/type checker that has been implemented by our collaborators in UFPE/Brazil. Our efforts created a heavy load of tests that have identified a couple of errors in this tool. These errors are listed in Section 2.2 and they have already been reported to the developers of the CSP_M parser, who have committed themselves to fix these bugs.

Our translator needs some extra information from the user as, for instance, the number of bits that are used to represent integers. These are given by the user to *csp2hc* in the form of directives (comments in the source *CSP_M* specification with a special format). In Section 2.3, we describe the directives that are used by *csp2hc*.

Our main objective is to fully automate the translation of the CMOS. With this purpose, we have created three milestones: the phase controller, the heap model, and the CMOS. In Section 3, we describe the efforts in the translation of these milestones: we describe the *CSP_M* constructors whose translation are already mechanised, and the ones whose translation have not yet been mechanised. For those whose translation have not yet been mechanised, we provide possible solutions for their implementation in **Handel-C**.

Finally, Section 4 present *csp2hc* and discuss how it can be used.

2 Current Status

csp2hc uses a *CSP_M* parser/type checker that has been implemented by our collaborators in UFPE/Brazil. This parser, with exception of a few constructors in which it presented some problems, already accepts all the *CSP_M* constructors needed for the full translation of the CMOS.

The input to *csp2hc* is a *CSP_M* specification that has already been checked in FDR. Furthermore, *csp2hc* considers that none of the **Handel-C** keywords are present in the source *CSP_M*. Besides, some further keywords are used by *csp2hc* and cannot be part of the input *CSP_M* specification as well. They are:

- clock1
- SYNC
- syncout
- syncin
- integer
- integer_offset
- For every possible integer value i:
 - $i \geq 0$: integer_i_s
 - $i < 0$: integer_neg_i_s
- boolean
- true
- false
- true_s

- `false_s`
- For every type `T` in the specification (including `integer` and `boolean`):
 - `T_set`
 - `T_nil`
 - `T_card`
 - `T_sets_LUT`
- For every simple datatype value `v`
 - `v`
 - `v_s`
- For every complex datatype value `C.v1.v2`, where `C` is the constructor declared as `C.T1.T2`
 - `C_T1_T2_LUT`
 - `C_v1_v2`
 - `C_v1_v2_s`
 - If any of the values `vi` is a negative integer, then we have `neg_vi` instead.
- `MUTUAL_REC`
- `PROGRAM_COUNTER`
- `KEEP_LOOPING`

In what follows, we list the CSP_M constructors that are supported by *csp2hc*. For each one of them, we present the restrictions on these constructors, and an outline of the solutions implemented by the translator.

1. `SKIP`

- (a) **Restrictions:** None
- (b) **Solution:** translates to nothing.

2. `STOP`

- (a) **Restrictions:** None
- (b) **Solution:** translates to an infinite loop that does nothing.

3. Sequential composition

- (a) **Restrictions:** None
- (b) **Solution:** translates to a sequential composition.

4. Recursion

(a) Simple Recursion

i. **Restrictions:**

A. Only Tail Recursion

- ii. **Solution:** tail recursions are translated to a loop that iterates while a `KEEP_LOOPING` variable is `true`. In each iteration, the loop initially sets `KEEP_LOOPING` to `false`; the tail recursion sets this variable to `true`. Possible process arguments are declared as local copies, which are initialised before the beginning of the loop with the given value and are updated before the end of each iteration.

For instance, process $P(x) = c1 \rightarrow P(x+1)$ is translated as follows.

```
inline void P1 (integer x){
    boolean KEEP_LOOPING;
    integer P1_local_x;
    P1_local_x = x;
    KEEP_LOOPING = true;
    while(KEEP_LOOPING){
        KEEP_LOOPING = false;
        seq{
            seq{
                c! P1_local_x;
                P1_local_x = x + 1;
                KEEP_LOOPING = true;
            }
        }
    }
}
```

(b) Mutual Recursion

i. **Restrictions:**

A. Parallel composition (and interleaving) only in the main process given in the directive (as described in Section 2.3.3).

- ii. **Solution:** the solution is to transform the whole model into an action system like model. First, we declare all the processes parameters as global variables. Then, we declare a single method parametrised by a process counter that will represent the whole system. Its body is a loop on a variable `KEEP_LOOPING`. In each iteration, we check the value of the program counter and behave accordingly. Possible process arguments are declared as global copies, which are initialised before each invocation of the mutual recursion.

For instance, let us consider the following specification:

$P1(x) = c!x \rightarrow P2(x+1)$
 $P2(x) = c!x \rightarrow P1(x-1)$

It is translated as follows.

```

integer P1_local_x, P2_local_x;
inline void MUTUAL_REC(int 1 PROGRAM_COUNTER){
    KEEP_LOOPING = true;
    while(KEEP_LOOPING){
        KEEP_LOOPING = false;
        switch(PROGRAM_COUNTER){
            case P1 :{
                seq{
                    c! P1_local_x;
                    P2_local_x = x + 1;
                    PROGRAM_COUNTER = P2;
                    KEEP_LOOPING = true;
                }
                break;
            }

            case P2 :{
                seq{
                    c! P2_local_x;
                    P1_local_x = x - 1;
                    PROGRAM_COUNTER = P1;
                    KEEP_LOOPING = true;
                }
                break;
            }
        }
    }
}

```

5. Prefixing

(a) Restrictions:

- i. Synchronisations of the form

$$\text{channel_name}[\text{csp_expression}]^*[\text{?var_name} \mid \text{!csp_expression}]^{0..1}$$

where csp_expression is as in Section 10a.

- ii. Projections are used consistently. For instance, if a channel is used as $c.e$, it cannot be used as $c!e$ elsewhere in the specification.

(b) Solution:

- i. Communications are translated to **Handel-C** communications
- ii. Simple synchronisations are translated to communications of dumb values. A directive indicates if the channel is an input or output
- iii. Synchronisations $c.e$ are translated to an access to the e -th element of an array c of channels. For each type T in the system, we declare a constant T_card that contains the number of element of elements in that type. This constant is used in the declaration of the array. Besides, signed integers are cast into unsigned integers.

6. External Choice

(a) **Restrictions:**

- i. Only for prefixing processes.
- ii. No two branches in an external choice with an on the input variables of the same name.

(b) **Solution:** translate to **Handel-C** `pralt`

7. Concurrency

(a) **Restrictions:**

- i. No multi-synchronisation

(b) Sharing Parallel

i. **Restrictions:**

- A. Only for processes with no interleaved events in the synchronisation set: for every two processes P and Q composed in parallel in a channel set CS ($P \parallel CS \parallel Q$), we have that $\alpha(P) \cap \alpha(Q) \subseteq CS$

ii. **Solution:** translate to **Handel-C** parallelism

(c) Alphanetised Parallel

i. **Restrictions:**

- A. Only for processes with no interleaved events in the synchronisation set: for every two processes P and Q synchronising in parallel in the channel sets $CS1$ and $CS2$ ($P \parallel CS1 \parallel CS2 \parallel Q$), we have that the synchronisation sets satisfy the condition $\alpha(P) \cap \alpha(Q) \subseteq CS1 \cap CS2$.

ii. **Solution:** translate to **Handel-C** parallelism

(d) Interleaving

i. **Restrictions:**

- A. Only for processes with no events in common: for every two processes P and Q interleaved ($P \parallel\!\!\parallel Q$), we have that $\alpha(P) \cap \alpha(Q) = \emptyset$

ii. **Solution:** translate to **Handel-C** parallelism

8. Datatypes

(a) **Restrictions:** No mutually recursive datatypes

(b) Simple datatypes

i. **Restrictions:** None

ii. **Solution:** the type is declared as an `unsigned int i`, where `i` is the number of bits need to represent the cardinality of the type. Each element of the datatype corresponds to an integer value (starting from 0). We also declare the cardinality of the type. By way of illustration, datatype `Alpha = a | b` is translated as follows.

```
#define Alpha unsigned int 1
#define b 0
#define a 1
#define Alpha_card 2
```

(c) Complex datatypes

i. **Restrictions:**

A. Constants cannot be given as argument to the constructors (tags)

B. Sets cannot be given as argument to the constructors (tags)

ii. **Solution:** constructors are seen as functions. For each element in the domain of the constructor there exists a corresponding value in the enumeration that corresponds to the datatype. For each possible constructor in the system, create a lookup table that, given the values of the domain of the constructor, returns the corresponding value in the enumeration of the datatype. For instance, let us consider the following datatype:

```
datatype Char = Letter.Alpha | Number.Int
```

The translation of this datatype is presented below:

```
#define Char unsigned int 3
#define Number_neg_1 0
#define Number_neg_2 1
#define Number_1 2
#define Number_0 3
#define Letter_a 4
#define Letter_b 5
#define Char_card 6 static Char
Char_Number_LUT[integer_card] ={
    Number_0 ,Number_1 ,Number_neg_2 ,Number_neg_1};
static Char Char_Letter_LUT[Alpha_card] ={
    Letter_b ,Letter_a};
```

(d) Int

i. **Restrictions:** None

- ii. **Solution:** Declare a constant `integer` that represents the integer within the Handel-C code as an `int` of `n` bits, where `n` is given as a directive and represents the number of bits in the representation of integers within the system.

(e) `Bool`

- i. **Restrictions:** None
- ii. **Solution:** Declare the constants `true` as 1 and `false` as 0, and declare `boolean` as `unsigned int 1`

(f) `Sets`

- i. **Restrictions:**
 - A. Cannot be used as channels type
 - B. Sets cannot be used in a channel usage
- ii. **Solution:** we use a bit presentation for sets. For every type `T` in the system we declare a constant `T_set unsigned int T_card`; furthermore, for every element `e`, we declare a singleton set `e_s`; finally, we declare the empty set `T_nil_s`. For instance, for datatype `Alpha = a | b`:

```
#define Alpha_set unsigned int Alpha_card
#define a_s 0b10
#define b_s 0b01
#define Alpha_nil_s 0b00
```

We also declare a lookup table `M_sets_LUT` that that returns singleton sets for every possible value in the system. When reading elements of a set, each element `e` is translated to `M[e]`; we make the bitwise logical or of the translation of every element.

9. Constants

- (a) **Restrictions:** None
- (b) **Solution:** translate to a Handel-C macro expression

10. Expressions

(a) **Restrictions:** just in the following syntax

csp_expression	::=	logical_expression
		math_expression
		rel_expression
		datatype_member
logical_expression	::=	true false
		logical_expression and logical_expression
		logical_expression or logical_expression
		not logical_expression
math_expression	::=	[0 .. 9] ⁺
		- math_expression
		math_expression + math_expression
		math_expression - math_expression
		math_expression * math_expression
		math_expression / math_expression
		math_expression % math_expression
rel_expression	::=	math_expression == math_expression
		math_expression != math_expression
		math_expression > math_expression
		math_expression >= math_expression
		math_expression < math_expression
		math_expression <= math_expression

(b) **Solution:** translate to the corresponding Handel-C expression

11. if _ then _ else _

(a) **Restrictions:** None

(b) **Solution:** Use Handel-C's if (_) { _ } else { _ }.

2.1 Restrictions Verification

csp2hc is able to automatically verify most of the restrictions currently imposed on the accepted constructors. This means that if any of these restrictions is not satisfied by the input CSP_M , *csp2hc* indicates the error to the user. The verified restrictions are:

- Syntax of expressions (10a)
- Accepted prefixing formats (5(a)i)
- Consistent use of channel projections (5(a)ii)
- No mutually recursive datatypes (8a)
- No constant are given to datatype constructors (8(c)iA)

- No sets are given to datatype constructors (8(c)iB)
- No two branches in an external choice with an on the input variables of the same name (6(a)ii)
- No non-tail recursive processes (4(a)iA)
- Parallel composition on mutually recursive processes only in the main process (4(b)iA)
- No channel sets used in channel declarations (8(f)iA)
- No linked parallelism

However, for timing restrictions only, some restrictions are not being checked by *csp2hc*; in this case, if any of these restrictions is not satisfied by the input CSP_M , *csp2hc* will generate a Handel-C code that does not implement correctly the original CSP_M specification.

- Not all unsupported constructors are being identified by *csp2hc*
- External choice only between prefixing processes (6(a)i)
- Restrictions on the synchronisation channel sets of shared parallel composition (7(b)iA), alphabetised parallel composition (7(c)iA), and interleaving (7(d)iA)
- Sets cannot be used in a channel usage 8(f)iB

2.2 Identified Errors in the Parser/Type Checker

Due to our big number of tests, we have identified a couple of errors in the CSP_M parser/type checker. In what follows, we describe these errors. For some of them, we provide temporary solutions.

- No comments within a CSP_M definition
- The type checker is raising an exception in the invocation of the following parametrised process:

$$P(x) = c \rightarrow P(x)$$

Temporary solution: declare a constant x in the CSP_M .

- The type checker is raising an exception in the invocation of the following parametrised process:

$$P(x) = c \rightarrow P(x+1)$$

Temporary solution: declare a function `inc = \ x @ x + 1` in the CSP specification, and invoke `P(inc(x))` instead.

- Intervals (`Interval = {1..3}`) are not being accepted by the type checker
- Datatypes constructors are accepting constants and they shouldn't
`datatype Alpha = a | b | Z.ZERO`, where `ZERO = 0`
- No constructor `Set`
- Sets of elements of a datatype are raising a
`CspSemanticException: referenced name 'a' was not declared`

```
datatype Letter = a | b
SET_LETTER_1 = {a, b}
```

In the near future we will receive a new version of the CSP_M parser/type checker that will include the corrections of these errors.

2.3 Directives

In order to be able to translate the source CSP_M code, our translator needs some extra information from the user. These are called directives, and are input in the form of comments in the source CSP_M specification with a special format. This format is a line commented as follows:

```
--!! DIRECTIVE
```

In what follows we discuss the current directives used by *csp2hc*.

2.3.1 Input and Output Channels

This directive is used to give to *csp2hc* the indication that a channel, which is not explicitly used as an input or as an output, is either an input or an output.

- Format: `--!! channel channel_name [in , out]1 within process_name`
- Mandatory: for every channel c used as an synchronisation event anywhere in the system

For instance, in the following input:

```
--!! channel c in within P
P = c -> SKIP
```

The directive indicates that c is an input channel in process P .

2.3.2 Argument type

This directive is used to give to *csp2hc* the type of each of the arguments of a parametrised process.

- Format: `--!! arg variable_name handelc_type within process_name`
- Mandatory: for every process argument

For instance, in the following input:

```
--!! arg x integer within P
--!! arg y Alpha within P
P(x,y) = c.x!y -> SKIP
```

The directive indicates that the types of arguments *x* and *y* of *P* are Handel-C's *integer* and *Alpha*, respectively.

2.3.3 Main Process

This directive is used to give to *csp2hc* the main process, which represents the main behaviour of the system.

- Format: `--!! main csp_process_expression`
- Optional: Default is *SKIP*

For instance, in the following input:

```
--!! main P [| {| c |} |] Q

--!! channel c in within P
P = c -> SKIP

--!! channel c in within Q
Q = c -> SKIP
```

The directive indicates that system behaves like the parallel composition of *P* and *Q*.

2.3.4 Number of bits for integers

This directive is used to give to *csp2hc* the number of bits used to represented integer number in the system.

- Format: `--!! int_bits [1..9]+1[0..9]*`
- Optional: Default is 1

For instance, in the following input:

```
--!! int_bits 2
```

The directive indicates that integers numbers in the system are those signed integer number that can be represented using two bits, thus -2, -1, 0, and 1 are valid integer numbers within this system.

Bit 1	Bit 0	Signed Number	Unsigned Number
0	0	0	0
0	1	1	1
1	0	-2	2
1	1	-1	3

Table 1: Unsigned and Signed Integers

Warning The number of bits declared by this directive must be sufficient to include the evaluation of all integer expressions within the specification. Otherwise, this inconsistency generates the following problem in the generated code. As previously described, if we declare integers to be of 2 bits, we are considering -2, -1, 0, and 1 as the possible values for integers in the specifications. If, however, in some point of the specification the values 2 and 3 are used, the **Handel-C** compiler accepts the generated code, but it interprets these values as -2 and -1, respectively. So, the specification of a parallel composition of the events `c.-2` and `c.2` does not synchronise, but the generated **Handel-C** code will synchronise. Although the **Handel-C** compiler should not accept such behaviour, the table 1 gives an insight why such behaviour happens: when unsigned, the numbers 2 and 3 have the same bitwise representation as the signed numbers -2 and -1, respectively.

2.3.5 Type of Empty Sets

Type inferencing is not possible for empty sets. For this reason, *csp2hc* needs a directive that indicates the type of empty sets. Currently, this is given for each process.

- Format: `--!! empty_set_of handelc_type within [process_name | datatype_name | constant_name]`
- Mandatory: for every empty set used in the system

For instance, in the following input:

```
--!! empty_set_of integer within P
P = c!{} -> SKIP
```

The directive indicates that empty set within P is an empty set of `integers`.

3 Translating CMOS

The final objective of the project is to automatically translate the whole of the CMOS specification; however, due to the complexity of the CMOS, we have drawn three milestones for the project: the phase controller, the heap model, and the CMOS. In what follows, we indicate for each of these milestones, the constructors whose translation are already mechanised, and the ones whose translation have not yet been mechanised. For those whose translation have not yet been mechanised, we provide possible solutions for their implementation in **Handel-C**.

3.1 Phase Controller

The constructors needed for the translation of the phase controller are:

1. Mutual Recursion (Requirement 4b)
2. Prefixing (Requirement 5)
3. External choice (Requirement 6)
4. Simple datatypes (Requirement 8b)

The current version of *csp2hc* is already able to automatically translate the phase controller.

3.2 Heap Model

The current version of *csp2hc* is already able to automatically translate part of the constructors used in the heap model. These constructors are:

1. **STOP** (Requirement 2)
2. Mutual recursion (Requirement 4b)
3. Prefixing (Requirement 5)
4. External choice (Requirement 6)
5. Sharing parallel (Requirement 7b)
6. Simple datatype (Requirement 8b)
7. **Int** (Requirement 8d)
8. **Bool** (Requirement 8e)
9. Constants (Requirement 9)

For timing restrictions only, the translation of the following constructors are not yet mechanised.

1. Support to multi-synchronisation
2. Sets can be given to datatype constructors
3. Boolean guards can take part in the external choice
4. Set expressions
 - (a) Sets as channel types
 - (b) Sets in communications

- (c) Sets as arguments
 - (d) Set comprehension
 - (e) Integer ranges
 - (f) `diff`
 - (g) `card`
 - (h) `member`
 - (i) `set`
 - (j) `pick`
5. Declaration of functions
 6. Declaration of `nametype`
 7. `let _ within _`
 8. Constrained inputs
 9. Pattern matching
 10. Sequences expressions
 - (a) Sequences Display
 - (b) Sequences concatenation
 - (c) `Seq`
 11. Tuples expressions

In what follows we discuss the solutions that will be implemented for each of these constructors.

Solutions

1. Support to multi-synchronisation
 - (a) **Restrictions:** None. Removes restriction 7(a)i
 - (b) **Solution:** implement multi-synchronisation using a protocol that uses a centralised controller. By way of illustration, let us consider the following three process:

$$\begin{aligned}
 P &= c!0 \rightarrow c?v_1 \rightarrow \text{SKIP} \\
 Q &= c?v_1 \rightarrow c!1 \rightarrow \text{SKIP} \\
 R &= c?v_1 \rightarrow d \rightarrow c?v_2 \rightarrow \text{SKIP}
 \end{aligned}$$

We can statically identify that `c` is a multi-synchronised channel in the parallel composition $P \parallel \{ \mid c, d \} \parallel (Q \parallel \{ \mid c, d \} \parallel R)$, and that it involves three processes. The three processes would be translated as follows:

```

inline void P (){
    seq{ offer_c[0]!syncout;
        seq{ offer_c_val! 0;
            seq{ integer dumb_int; c? dumb_int;
                seq{ offer_c[0]!syncout;
                    seq{ integer v_1;
                        c? v_1; }; }; }; }; }
}
inline void Q (){
    seq{ offer_c[1]!syncout;
        seq{ integer v_1; c? v_1;
            seq{ offer_c[1]!syncout;
                seq{ offer_c_val! 1;
                    seq{ integer dumb_int;
                        c? dumb_int;
                            }; }; }; }; }
}
inline void R (){
    seq{ offer_c[2]!syncout;
        seq{ integer v_1; c? v_1;
            seq{ d!syncout;
                seq{ offer_c[2]!syncout;
                    seq{ integer v_2; c? v_2;
                        }; }; }; }; }
}

```

where `dumb_int` is a fresh variable name.

The code for the controller would be as follows:

```

inline void c_MS_controller() {
    boolean stopped_0, stopped_1, stopped_2, stopped;
    par { stopped_0 = false; stopped_1 = false; stopped_2 = false; };
    stopped = stopped_0 || stopped_1 || stopped_2;
    while (!stopped) {
        seq{ par { stopped_0 = c_MS_controller_wait_for(0);
                    stopped_1 = c_MS_controller_wait_for(1);
                    stopped_2 = c_MS_controller_wait_for(2); };
            stopped = stopped_0 || stopped_1 || stopped_2;
            if (!stopped) {
                seq { integer v; offer_c_val? v; c! v; }
            }
        }
    }
}

```

```

inline boolean c_MS_controller_wait_for(integer i) {
    boolean stopped; stopped = false;
    prialt{
        case stop_c?syncin :{ stopped = true; };
        break;
        case offer_c[(unsigned)i]?syncin:{ ; };
        break;
    }
    return stopped;
}

```

The parallel composition $P \parallel \{c, d\} \parallel (Q \parallel \{c, d\} \parallel R)$ is then implemented as follows:

```

par{
    seq{ par{ P(); Q(); S(); };
        stop_c!syncout; };
    c_MS_controller();
}

```

2. Sets can be given to datatype constructors

- (a) **Restrictions:** None. It removes restriction 8(c)iB
- (b) **Solution:** Since this specification has already been checked by FDR, we can use the maximal type of the set instead of the set itself. For this, we need to infer the type of the set; the types of empty sets need to be given via a directive.

3. Boolean guards can take part in the external choice

- (a) **Restrictions:** None. It relaxes restriction 6(a)i
- (b) **Solution:** Use the following transformation before translation

$$(g \ \& \ P) \parallel Q = \text{if } g \text{ then } (P \parallel Q) \text{ else } Q$$

4. Set expressions

- (a) Sets as channel types
 - i. **Restrictions:** None. It removes restrictions 8(f)iA
 - ii. **Solution:** Since this specification has already been checked by FDR, we can use the maximal type of the set instead of the set itself. For this, we need to infer the type of the set; the types of empty sets need to be given via a directive.

- (b) Sets in communications
 - i. **Restrictions:** None
 - ii. **Solution:** Sets are the bitwise or of the corresponding singleton sets (in the lookup table) of the elements.
- (c) Sets as arguments
 - i. **Restrictions:** None
 - ii. **Solution:** Sets are the bitwise or of the corresponding singleton sets (in the lookup table) of the elements.
- (d) Set comprehension
 - i. **Restrictions:** None
 - ii. **Solution:** Create a library in Handel-C that allows the translation of such constructions
- (e) Integer ranges
 - i. **Restrictions:** None
 - ii. **Solution:** Create a bitwise or of every integer from the minimum to the maximum value.
- (f) `diff`
 - i. **Restrictions:** None
 - ii. **Solution:** `macro expr diff(s,t) = (s & (~t));`
- (g) `card`
 - i. **Restrictions:** None
 - ii. **Solution:** Create a lookup table containing 0 and 1.


```
static integer card_LUT[2] = {0 , 1};
```

Declare the following macro, where `n` is the maximum integer value, based on the given directive.

```
macro expr card(b) = (LUT[b[0]] + LUT[b[1]] + ... + LUT[b[n]]);
```
- (h) Union
 - i. **Restrictions:** None
 - ii. **Solution:** Bitwise or of all the elements, which are themselves sets.
- (i) `member`
 - i. **Restrictions:** None
 - ii. **Solution:** First, for every type, there will be a lookup table that returns the singleton set that contain each one of the elements in that type. For instance, for the booleans we have:


```
static boolean_set singleton_boolean_sets_LUT[boolean_card] ={
    true_s, false_s
};
```

For the integers (i.e. 2 bits integers), the lookup table will look like this:

```
static integer_set singleton_integer_sets_LUT[integer_card] = {
    integer_0_s, integer_1_s,
    integer_neg_2_s, integer_neg_1_s
};
```

Then, the set membership will be given by the following macro expression:

```
macro expr member(e,s) =
    ((singleton_integer_sets_LUT[(unsigned)e] | s) == s);
```

(j) **set**

- i. **Restrictions:** None
- ii. **Solution:** returns the bitwise or of all the elements. For instance, `set([-1,0,1,2])` is

```
singleton_integer_sets_LUT[(unsigned)-1] |
singleton_integer_sets_LUT[(unsigned)0] |
singleton_integer_sets_LUT[(unsigned)1] |
singleton_integer_sets_LUT[(unsigned)2]
```

(k) **pick**

- i. **Restrictions:** None
- ii. **Solution:** we illustrate our solution with a set of a type with cardinality eight. These are the possible singleton sets **b**, and the binary representation of the element **x** of the singleton set.

b_7	b_6	b_5	b_4	b_3	b_2	b_1	b_0	x_2	x_1	x_0
1	0	0	0	0	0	0	0	1	1	1
0	1	0	0	0	0	0	0	1	1	0
0	0	1	0	0	0	0	0	1	0	1
0	0	0	1	0	0	0	0	0	1	0
0	0	0	0	1	0	0	0	0	1	1
0	0	0	0	0	1	0	0	0	1	0
0	0	0	0	0	0	1	0	0	0	1
0	0	0	0	0	0	0	1	0	0	0

From this table, we notice that we actually have a pattern in which, `pick(s) = x[2] @ x[1] @ x[0]`, where $x_i = \bigvee \{b_j \mid x_i = 1 \text{ in binary representation of } j\}$. In our example, we have that:

```
x[2] = b[7] || b[6] || b[5] || b[4]
x[1] = b[7] || b[6] || b[3] || b[2]
x[0] = b[7] || b[5] || b[3] || b[1]
```

Thus, `pick(0b01000000) = 0b110 = 6`

5. Declaration of functions

- (a) **Restrictions:** None
- (b) **Solution:** Declare as macro expressions

6. Declaration of `nametype`

- (a) **Restrictions:** None
- (b) **Solution:** Use Handel-C's `typedef`.

7. `let _ within _`

- (a) **Restrictions:** None
- (b) **Solution:** Declare one global macro for each of the local variables in the order they appear

8. Constrained inputs

- (a) **Restrictions:** None
- (b) **Solution:** two solutions have already been considered. They, however, need further improvements in order to achieve a more general solution.

- i. Provided we have **only one-to-one communications**, once we find a constrained input in the program tree, we have to go back up to the first parallel composition and do the following transformation from there.

```
(c!e -> Q [] i_l -> R)
[] {| c |} |
(c?x:S -> P(x) [] i_r -> T)
=
( (try!e -> (c!e -> Q [] i_l -> R))
  [] {| c , try |} |
  (try?x -> if member(x,S) then c?x -> P(x) [] i_r -> T
           else i_r -> T) ) \ {| try |}
```

Notice that we consider `try` to be a fresh channel name and `y` to be a fresh variable name within the protocol. Otherwise we index their names with the first integers `n` and `m`, such that `try_n` and `y_m` are fresh names within the system. **However**, if this parallel composition is in parallel with another action, as the one below

```
c?x -> V [] i_m -> W
```

It may the case where the whole parallel composition in CSP allows `c` to happen; if `member(e,S)`. However, in Handel-C this may not happen because of the clock. It may the case in which some of the interruption

events `i_x` happens in the clock cycle inserted by the `try` event; this will cause the event `c` not to be allowed to happen.

- ii. Provided **the expression `e` is in terms only of global variables (parameters are NOT global)**, then we already have the value available in both sides; only the communication is needed.

```
(c!e -> Q [] i_l -> R)
[] {[] c []} []
(c?x:S -> P(x) [] i_r -> T)
=
(c!e -> Q [] i_l -> R)
[] {[] c []} []
( if member(e,S) then c?x -> P(x) [] i_r -> T
  else i_r -> T )
```

9. Pattern matching

- (a) **Restrictions:** The whole solution is equivalent to writing a functional language compiler. Just the pattern matching used in the CMOS will be accepted.
- (b) **Solution:**
 - i. For `c?_`, replace `_` by a fresh new name
 - ii. For `let (S,_,M,V) = E within P`, translate it as
`let S = E.1, M = E.3, V = E.4 within P`
 - iii. Remaining must be refined

10. Sequences

- (a) Sequences Display
 - i. **Restrictions:** None
 - ii. **Solution:** Sequences can be represented as arrays with a high water mark, using `struct`. A directive must be given in order to establish the maximum size of the array. So, for instance, if the maximum size of the sequences is said to be six, the sequence `[1,2,3]` is represented as `[_,_,_,(1),2,3]`, with the high water mark set to 3 (we denote the high water mark by putting the element on which the mark is between parenthesis. Furthermore, we write `_` when the value can be any value).
- (b) Sequences concatenation
 - i. **Restrictions:** None
 - ii. **Solution:** simply introduce the elements of the left-hand side sequence to the right hand side array as follows.

<code>[1,2] ^ [4,5,6]</code>	[Representation of sequences]
<code>= [_,_,_,_,(1),2] ^ [_,_,_,_,(4),5,6]</code>	[Calculation]

$$\begin{aligned}
&= [_,_,_,_,(1),_] \hat{~} [_,_,_,(2),4,5,6] && \text{[Calculation]} \\
&= [_,_,_,_,_,_] \hat{~} [_,_,(1),2,4,5,6] && \text{[Base case]} \\
&= [_,_,(1),2,4,5,6]
\end{aligned}$$

(c) Seq

- i. **Restrictions:** None
- ii. **Solution:** as we have a maximum number of elements for the sequences, the sets of sequences is not infinite as in the CSP. In order to reuse the already existing translation strategies, we declare a datatype whose elements are all the possible sequences and translate this datatype. For instance, suppose we have three for the maximum length of the sequences. In this case, `Seq(Bool)` will be translated as the following datatype:

```

datatype SEQ_Bool ==
  SEQ_Bool_empty
  | SEQ_Bool_true | SEQ_Bool_false
  | SEQ_Bool_true_true | SEQ_Bool_true_false
  | SEQ_Bool_false_true | SEQ_Bool_false_false

```

A lookup table has also to be provided in order to construct the elements of this type.

```

static SEQ_Bool SEQ_Bool_LUT[7] =
  {[_,_] ,
   [_,true], [_,false],
   [true,true], [true,false], [false,true], [false,false]};

```

11. Tuples

(a) Tuples Display

- i. **Restrictions:** None
- ii. **Solution:** Tuples can be translated using `struct`.

3.3 CMOS

The current version of *csp2hc* is already able to automatically translate part of the constructors used in the CMOS. These constructors are:

1. SKIP (Requirement 1)
2. STOP (Requirement 2)
3. Sequential Composition (Requirement 3)
4. Recursion (Requirement 4)

5. Prefixing (Requirement 5)
6. External choice (Requirement 6)
7. Concurrency (Requirement 7)
8. Datatypes (Requirement 8)
9. Constants (Requirement 9)

For timing restrictions only, besides those discussed in Section 3.2 the translation of the following constructors are not yet mechanised.

1. Channel sets
2. Renaming
 - (a) Non-relational Renaming
 - (b) Relational Renaming
3. `include`
4. `chase`
5. Remove restriction on the synchronisation channel set of sharing parallel composition (Restriction 7(b)iA) and interleaving (Restriction 7(d)iA)
6. Indexed sequence
7. Indexed parallelism
8. Replicated choices
9. `module`
10. Sequence comprehension

In what follows we discuss the solutions that will be implemented for each of these constructors.

1. Channel sets
 - (a) **Restrictions:** None
 - (b) **Solution:** as for requirement 10c, we will translate a datatype that contains all the possible channels within the system. For instance, for a system containing the following channels


```
channel b channel c: Int
```

and assuming two bits integers, we have the following datatype.

```
CHANNELS == CHANNEL_b
           | CHANNEL_c_neg_2 | CHANNEL_c_neg_1
           | CHANNEL_c_0 | CHANNEL_c_1
```

2. Renaming

(a) Non-relational Renaming

- i. **Restrictions:** None
- ii. **Solution:** By replacing as the following example.
 $P = Q[a \leftarrow b]$

- Get the body of Q
- Replace a by b (let's call this $NewQ$ in this example)
- Translate $P = NewQ$

(b) Relational Renaming

- i. **Restrictions:** to be analysed
- ii. **Solution:** to be analysed

3. include

- (a) **Restrictions:** None
- (b) **Solution:** Append files before parsing.

4. chase

- (a) **Restrictions:**
- (b) **Solution:** Provided **we have no use of the internal choice operator**, the only τ events are generated by hiding. In this case, every time we find a hiding $P \setminus cs$ in the tree, we analyse P : every external choice in P must be rewritten such that any initial channel in the choice that is hidden must come first in the choice. For instance:

$$((a \rightarrow P) [] (b \rightarrow Q)) \setminus \{| b | \}$$

must be rewritten as

$$((b \rightarrow Q) [] (a \rightarrow P)) \setminus \{| b | \}$$

Since we use **PRIALT** to implement choice, b will be given priority, which in the end, means that we are given priority to the τ event.

5. Remove restriction on the synchronisation channel set of sharing parallel composition (Restriction 7(b)iA) and interleaving (Restriction 7(d)iA)
 - (a) **Restrictions:** to be analysed
 - (b) **Solution:** to be analysed
6. Indexed sequence
 - (a) **Restrictions:** Only closed range numerical indexes for indexed sequences
 - (b) **Solution:** For every basic `datatype` `d` within the model there will be a function $mapping_d : d \rightarrow \mathbb{N}$.
7. Indexed parallelism
 - (a) **Restrictions:** Only closed range numerical indexes for indexed parallel
 - (b) **Solution:** The same as 6
8. Replicated choices
 - (a) **Restrictions:** Only closed range numerical indexes for replicated choices
 - (b) **Solution:** translate the expansion of the replicated choice
9. `module`
 - (a) **Restrictions:** Only closed range numerical indexes for replicated choices
 - (b) **Solution:** flatten the whole specification before translation, renaming the module components in order to avoid name clashes.
10. Sequence comprehension
 - (a) **Restrictions:** None
 - (b) **Solution:** Create a library in `Handel-C` that allows the translation of such constructions

Once the translation of all these constructors are implemented the translation of the CMOS into `Handel-C` will be fully automated. Besides, the vast majority of the CSP constructs will have been translated and, as such, a large number of CSP_M specifications will be automatically translated into `Handel-C`.

4 Using *csp2hc*

In order to execute *csp2hc*, simply execute the file `csp2hc.bat`. The JVM used must be of version `1.5.0_06` or higher. The interface is very simple and presented in Figure 1: it is composed, basically, by a log window, in which all the stages of the translation are logged. In order to use the translator, simply open the CSP file (`.csp`) you want to translate and, if the translation is successful, save the result in a `Handel-C` (`.hcc`) file; the user is then

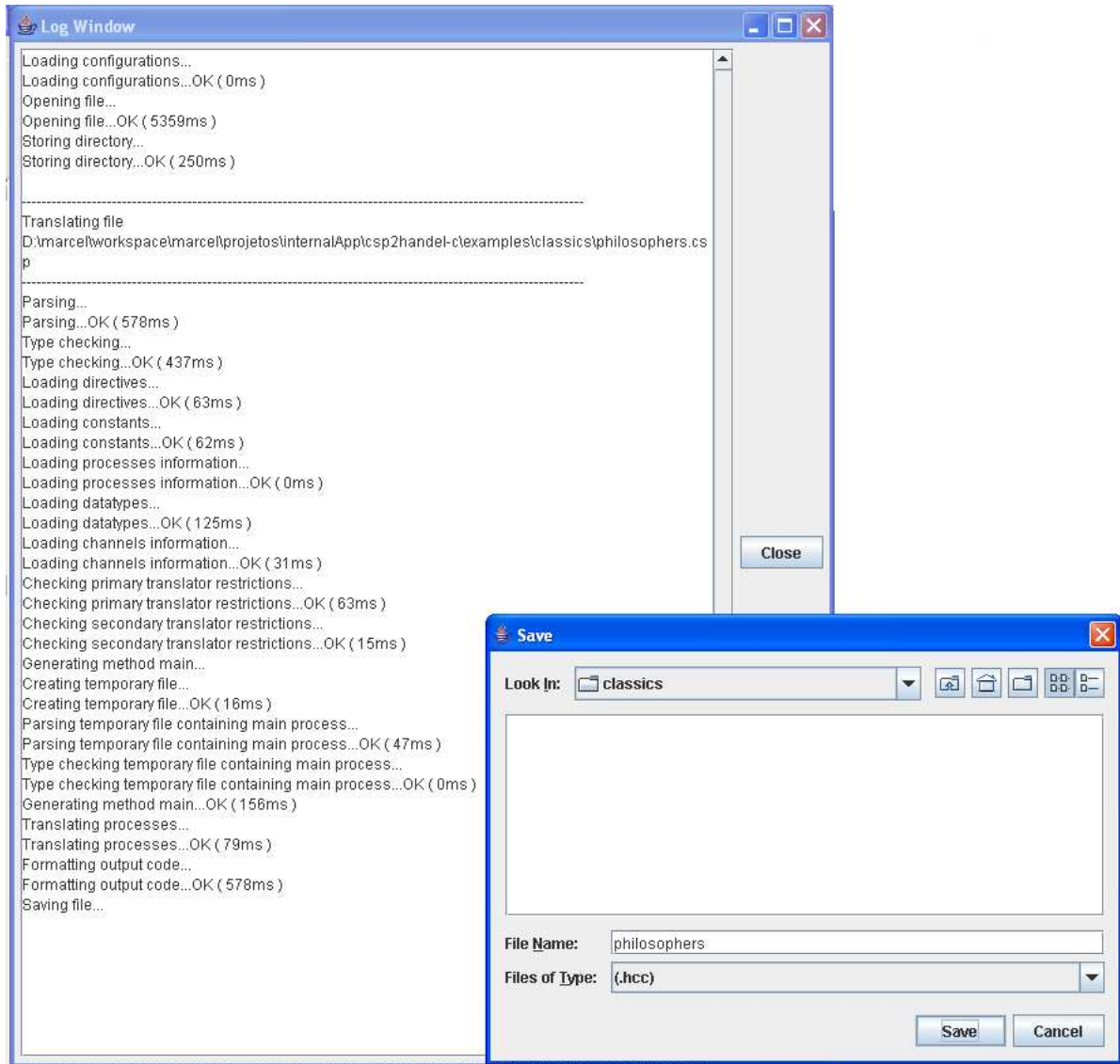


Figure 1: *csp2hc* Graphic Interface

given the choice of translating another file. If, however, the translation is not successful, an error message is given, and the reason for the error is shown in the log window. The user is given the choice of correcting the file and trying to translate it again. In order to finish the execution of the tool, simply click `close` in the log window.

In the future, the interface could be incremented. For instance, it could have three tabs: the translation log, the source CSP file, and the target Handel-C file. This would allow the user to edit the files without the need of an extra text editor.

5 Further requirements

In this project, we have concentrated on the features and requirements of the CMOS; however, some constructs are not used in the CMOS and were not considered. They are listed below.

1. Two processes writing to the same channel on the same clock cycle
2. The same channels is involved in a choice, it must be in the same direction.
3. Lambda terms
4. Interrupts
5. `CHAOS`
6. `/\`
7. Untimed time out
8. `external`
9. Nested blocks of comment markers

In order to achieve a full automatic translation from CSP_M to Handel-C these are some of the constructs that still need to be taking into account.

6 Transcripts

6.1 Dining Philosophers

6.1.1 CSP_M

```
----- 1
-- DINING PHILOSOPHERS -- 2
----- 3

--!! main TABLE_P 4
--!! int_bits 4 5
-- To get around the type checker 6
i = 0 7
j = 0 8

-- Maximum number of Philosophers 9
MAX_PHIL = 5 10

----- 11
-- FUNCTIONS -- 12
----- 13

-- Decrements i by one modulo j 14
dec_mod = \ i,j @ (i - 1) % j 15

-- Increments i by one modulo j 16
inc_mod = \ i,j @ (i + 1) % j 17

-- Returns the maximum between i and j 18
max = \ i,j @ if i > j then i else j 19

-- Returns the minimum between i and j 20
min = \ i,j @ if i < j then i else j 21

----- 22
-- CHANNELS -- 23
----- 24

channel thinks, sits, eats, getsup:Int 25
-- FOR FDR CHECK, COMMENT LINE ABOVE AND UNCOMMENT LINE BELOW 26
--channel thinks, sits, eats, getsup:{0..MAX_PHIL-1} 27
----- 28
----- 29
----- 30
----- 31
----- 32
----- 33
----- 34
----- 35
----- 36
----- 37
----- 38
----- 39
```



```

1
2 channel pickup, putdown: Int.Int
3 -- FOR FDR CHECK, COMMENT LINE ABOVE AND UNCOMMENT LINE BELOW
4 --channel pickup, putdown:{0..MAX_PHIL-1}.{0..MAX_PHIL-1}
5
6 -----
7 -- FORKS --
8 -----
9 --!! channel pickup in within FORK
10 --!! channel putdown in within FORK
11 --!! arg i integer within FORK
12 FORK(i) =
13     pickup.i.i -> putdown.i.i -> FORK(i)
14     []
15     pickup.dec_mod(i,MAX_PHIL).i ->
16         putdown.dec_mod(i,MAX_PHIL).i -> FORK(i)
17
18 -----
19 -- PHILOSOPHERS --
20 -----
21 --!! channel pickup out within PHIL
22 --!! channel putdown out within PHIL
23 --!! channel thinks out within PHIL
24 --!! channel sits out within PHIL
25 --!! channel eats out within PHIL
26 --!! channel getsup out within PHIL
27 --!! arg i integer within PHIL
28 PHIL(i) =
29     thinks.i -> sits.i ->
30         pickup.i.min(i,inc_mod(i,MAX_PHIL)) ->
31             pickup.i.max(i,inc_mod(i,MAX_PHIL)) -> eats.i ->
32                 putdown.i.inc_mod(i,MAX_PHIL) ->
33                     putdown.i.i -> getsup.i -> PHIL(i)
34
35 PHIL_FORK_SYNC = {| pickup, putdown |}
36
37
38
39
40
41
42
43
44

```

```

-----
-- ENVIRONMENT --
-----

--!! channel thinks in within THINKS
--!! arg i integer within THINKS
THINKS(i) = thinks.i -> THINKS(i)
THINKING = ( THINKS(0)
            |||
            (THINKS(1) ||| (THINKS(2) ||| (THINKS(3) ||| THINKS(4)))) )

--!! channel sits in within SITS
--!! arg i integer within SITS
SITS(i) = sits.i -> SITS(i)
SITTING = (SITS(0) ||| (SITS(1) ||| (SITS(2) ||| (SITS(3) ||| SITS(4))))))

--!! channel eats in within EATS
--!! arg i integer within EATS
EATS(i) = eats.i -> EATS(i)
EATING = (EATS(0) ||| (EATS(1) ||| (EATS(2) ||| (EATS(3) ||| EATS(4))))))

--!! channel getsup in within GETSUP
--!! arg i integer within GETSUP
GETSUP(i) = getsup.i -> GETSUP(i)
GETTINGUP = ( GETSUP(0)
              |||
              (GETSUP(1) ||| (GETSUP(2) ||| (GETSUP(3) ||| GETSUP(4)))) )

ENVIRONMENT_SYNC = {| thinks, sits, eats, getsup |}
ENVIRONMENT = THINKING ||| (SITTING ||| (EATING ||| GETTINGUP))

-----
-- TABLE --
-----

-- ALL PHILOSOPHERS
ALL_PHIL = PHIL(0) ||| (PHIL(1) ||| (PHIL(2) ||| (PHIL(3) ||| PHIL(4))))

-- ALL FORKS
ALL_FORK = FORK(0) ||| (FORK(1) ||| (FORK(2) ||| (FORK(3) ||| FORK(4))))

-- TABLE
TABLE = ALL_PHIL [| PHIL_FORK_SYNC |] ALL_FORK

```

```

1
2 -----
3 -- TABLE PROTOTYPE --
4 -----
5 TABLE_P = ENVIRONMENT [| ENVIRONMENT_SYNC |] TABLE

```

6.1.2 Handel-C

```

6 // -----
7 // Declaration of the system's clock
8 // -----
9 set clock = external "clock1";
10
11
12 // -----
13 // Declaration of the empty channel set
14 // -----
15 #define channels_nil_s 0b0
16
17 // -----
18 // Declaration of auxiliary constants for communication
19 // through synchronisation channels
20 // -----
21 typedef unsigned int 1 SYNC;
22 const SYNC syncout =0;
23 SYNC syncin;
24
25 // -----
26 // Declaration of constants related to the datatype boolean
27 // -----
28
29 // -----
30 // Declaration of constants related to the datatype boolean
31 // -----
32 #define boolean unsigned int 1
33 #define false 0
34 #define true 1
35 #define boolean_card 2
36 #define boolean_set unsigned int boolean_card
37 #define true_s 0b10
38 #define false_s 0b01
39 #define boolean_nil_s 0b00
40 static boolean_set boolean_sets_LUT[boolean_card] ={
41     false_s,true_s};

```

```

// ----- 1
// Declaration of constants related to the datatype integer 2
// ----- 3
#define integer_int 4 4
#define integer_card 16 5
#define integer_offset integer_card / 2 6
#define integer_set unsigned int integer_card 7
#define integer_neg_1_s 0b1000000000000000 8
#define integer_neg_2_s 0b0100000000000000 9
#define integer_neg_3_s 0b0010000000000000 10
#define integer_neg_4_s 0b0001000000000000 11
#define integer_neg_5_s 0b0000100000000000 12
#define integer_neg_6_s 0b0000010000000000 13
#define integer_neg_7_s 0b0000001000000000 14
#define integer_neg_8_s 0b0000000100000000 15
#define integer_7_s 0b0000000010000000 16
#define integer_6_s 0b0000000001000000 17
#define integer_5_s 0b0000000000100000 18
#define integer_4_s 0b0000000000010000 19
#define integer_3_s 0b0000000000001000 20
#define integer_2_s 0b0000000000000100 21
#define integer_1_s 0b0000000000000010 22
#define integer_0_s 0b0000000000000001 23
#define integer_nil_s 0b0000000000000000 24
static integer_set integer_sets_LUT[integer_card] = { 25
    integer_0_s, integer_1_s, integer_2_s, integer_3_s, integer_4_s, 26
    integer_5_s, integer_6_s, integer_7_s, integer_neg_8_s, integer_neg_7_s, 27
    integer_neg_6_s, integer_neg_5_s, integer_neg_4_s, integer_neg_3_s, 28
    integer_neg_2_s, integer_neg_1_s}; 29
// ----- 30
// Constants Prototypes 31
// ----- 32
macro expr ENVIRONMENT_SYNC; 33
macro expr PHIL_FORK_SYNC; 34
macro expr min(i,j); 35
macro expr max(i,j); 36
macro expr inc_mod(i,j); 37
macro expr dec_mod(i,j); 38
macro expr MAX_PHIL; 39
macro expr j; 40
macro expr i; 41

```

```

1 // -----
2 // Constants
3 // -----
4 macro expr i = (0);
5 macro expr j = (0);
6 macro expr MAX_PHIL = (5);
7 macro expr dec_mod(i,j) = (((i-1)%(j)));
8 macro expr inc_mod(i,j) = (((i+1)%(j)));
9 macro expr max(i,j) = (((i)>(j))? (i): (j) );
10 macro expr min(i,j) = (((i)<(j))? (i): (j) );
11 macro expr PHIL_FORK_SYNC = (channels_nil_s);
12 macro expr ENVIRONMENT_SYNC = (channels_nil_s);
13
14 // -----
15 // Declaration of the channels
16 // -----
17 chan SYNC thinks[integer_card];
18 chan SYNC sits[integer_card];
19 chan SYNC puttdown[integer_card][integer_card];
20 chan SYNC pickup[integer_card][integer_card];
21 chan SYNC eats[integer_card];
22 chan SYNC getsup[integer_card];
23
24 // -----
25 // Processes Prototypes
26 // -----
27 inline void ENVIRONMENT();
28 inline void SITTING();
29 inline void THINKS(integer i);
30 inline void EATTING();
31 inline void ALL_FORK();
32 inline void ALL_PHIL();
33 inline void GETSUP(integer i);
34 inline void TABLE();
35 inline void THINKING();
36 inline void GETTINGUP();
37 inline void SITS(integer i);
38 inline void TABLE_P();
39 inline void EATS(integer i);
40 inline void FORK(integer i);
41 inline void PHIL(integer i);
42
43
44

```

```

// ----- 1
// Process FORK 2
// ----- 3
inline void FORK (integer i){ 4
    boolean KEEP_LOOPING; 5
    integer FORK_local_i; 6
    FORK_local_i = i; 7
    KEEP_LOOPING = true; 8
    while(KEEP_LOOPING){ 9
        KEEP_LOOPING = false; 10
        seq{ 11
            prialt{ 12
                case picksup[(unsigned)(FORK_local_i)] 13
                    [(unsigned)(FORK_local_i)]?syncin :{ 14
                        seq{ 15
                            puttdown[(unsigned)(FORK_local_i)] 16
                                [(unsigned)(FORK_local_i)]?syncin; 17
                            FORK_local_i = i; 18
                            KEEP_LOOPING = true; 19
                        } 20
                    }; 21
                break; 22
                case picksup[(unsigned)(dec_mod(i,MAX_PHIL))] 23
                    [(unsigned)(FORK_local_i)]?syncin :{ 24
                        seq{ 25
                            puttdown[(unsigned)(dec_mod(i,MAX_PHIL))] 26
                                [(unsigned)(FORK_local_i)]?syncin; 27
                            FORK_local_i = i; 28
                            KEEP_LOOPING = true; 29
                        } 30
                    }; 31
                break; 32
            } 33
        } 34
    } 35
} 36

// ----- 37
// Process PHIL 38
// ----- 39
inline void PHIL (integer i){ 40
    boolean KEEP_LOOPING; 41
    integer PHIL_local_i; 42
    PHIL_local_i = i; 43
    44

```

```

1      KEEP_LOOPING = true;
2      while(KEEP_LOOPING){
3          KEEP_LOOPING = false;
4          seq{
5              seq{
6                  thinks[(unsigned)(PHIL_local_i)]!syncout;
7                  seq{
8                      sits[(unsigned)(PHIL_local_i)]!syncout;
9                      seq{
10                         pickup[(unsigned)(PHIL_local_i)]
11                             [(unsigned)(min(i,inc_mod ( i , MAX_PHIL )))]
12                             !syncout;
13                         seq{
14                             pickup[(unsigned)(PHIL_local_i)]
15                                 [(unsigned)(max(i,inc_mod ( i , MAX_PHIL )))]
16                                 !syncout;
17                         seq{
18                             eats[(unsigned)(PHIL_local_i)]!syncout;
19                             seq{
20                                 puttdown[(unsigned)(PHIL_local_i)]
21                                     [(unsigned)(inc_mod(i,MAX_PHIL))]
22                                     !syncout;
23                             seq{
24                                 puttdown[(unsigned)(PHIL_local_i)]
25                                     [(unsigned)(PHIL_local_i)]!syncout;
26                             seq{
27                                 getsup[(unsigned)(PHIL_local_i)]!syncout;
28                                 PHIL_local_i = i;
29                                 KEEP_LOOPING = true;
30                             };
31                         };
32                     };
33                 };
34             };
35         };
36     };
37 }
38 }
39 }
40 }
41
42
43
44

```

```

// ----- 1
// Process THINKS 2
// ----- 3
inline void THINKS (integer i){ 4
    boolean KEEP_LOOPING; 5
    integer THINKS_local_i; 6
    THINKS_local_i = i; 7
    KEEP_LOOPING = true; 8
    while(KEEP_LOOPING){ 9
        KEEP_LOOPING = false; 10
        seq{ 11
            seq{ 12
                thinks[(unsigned)(THINKS_local_i)]?syncin; 13
                THINKS_local_i = i; 14
                KEEP_LOOPING = true; 15
            } 16
        } 17
    } 18
} 19

// ----- 20
// Process THINKING 21
// ----- 22
inline void THINKING (){ 23
    par{ 24
        { THINKS(0); }; 25
        { par{ { THINKS(1); }; 26
            { par{ { THINKS(2); }; 27
                { par{ { THINKS(3); }; 28
                    { THINKS(4); } 29
                }; 30
            }; 31
        }; 32
    }; 33
}; 34
} 35
} 36
} 37
} 38
} 39
} 40
} 41
} 42
} 43
} 44

```



```

1 // -----
2 // Process SITS
3 // -----
4 inline void SITS (integer i){
5     boolean KEEP_LOOPING;
6     integer SITS_local_i;
7     SITS_local_i = i;
8     KEEP_LOOPING = true;
9     while(KEEP_LOOPING){
10         KEEP_LOOPING = false;
11         seq{
12             seq{
13                 sits[(unsigned)(SITS_local_i)]?syncin;
14                 SITS_local_i = i;
15                 KEEP_LOOPING = true;
16             }
17         }
18     }
19 }
20
21 // -----
22 // Process SITTING
23 // -----
24 inline void SITTING (){
25     par{
26         { SITS(0); };
27         { par{ { SITS(1); };
28             { par{ { SITS(2); };
29                 { par{ { SITS(3); };
30                     { SITS(4); }
31                 };
32             }
33         };
34     };
35 };
36 }
37 }
38 }
39
40
41
42
43
44

```

```

// ----- 1
// Process EATS 2
// ----- 3
inline void EATS (integer i){ 4
    boolean KEEP_LOOPING; 5
    integer EATS_local_i; 6
    EATS_local_i = i; 7
    KEEP_LOOPING = true; 8
    while(KEEP_LOOPING){ 9
        KEEP_LOOPING = false; 10
        seq{ 11
            seq{ 12
                eats[(unsigned)(EATS_local_i)]?syncin; 13
                EATS_local_i = i; 14
                KEEP_LOOPING = true; 15
            } 16
        } 17
    } 18
} 19

// ----- 20
// Process EATTING 21
// ----- 22
inline void EATTING (){ 23
    par{ 24
        { EATS(0); }; 25
        { par{ { EATS(1); }; 26
            { par{ { EATS(2); }; 27
                { par{ { EATS(3); }; 28
                    { EATS(4); } 29
                }; 30
            }; 31
        }; 32
    }; 33
}; 34
} 35
} 36
} 37
} 38
} 39
} 40
} 41
} 42
} 43
} 44

```

```

1 // -----
2 // Process GETSUP
3 // -----
4 inline void GETSUP (integer i){
5     boolean KEEP_LOOPING;
6     integer GETSUP_local_i;
7     GETSUP_local_i = i;
8     KEEP_LOOPING = true;
9     while(KEEP_LOOPING){
10         KEEP_LOOPING = false;
11         seq{
12             seq{
13                 getsup[(unsigned)(GETSUP_local_i)]?syncin;
14                 GETSUP_local_i = i;
15                 KEEP_LOOPING = true;
16             }
17         }
18     }
19 }
20
21 // -----
22 // Process GETTINGUP
23 // -----
24 inline void GETTINGUP (){
25     par{
26         { GETSUP(0); };
27         { par{ { GETSUP(1); };
28             { par{ { GETSUP(2); };
29                 { par{ { GETSUP(3); };
30                     { GETSUP(4); }
31                 };
32             }
33         };
34     };
35 };
36 }
37 }
38 }
39
40
41
42
43
44

```

```

// ----- 1
// Process ENVIRONMENT 2
// ----- 3
inline void ENVIRONMENT () { 4
    par { 5
        { THINKING(); }; 6
        { par { 7
            { SITTING(); }; 8
            { par { 9
                { EATING(); }; 10
                { GETTINGUP(); } 11
            }; 12
        } 13
    }; 14
} 15
} 16
} 17
} 18

// ----- 19
// Process ALL_PHIL 20
// ----- 21
inline void ALL_PHIL () { 22
    par { 23
        { PHIL(0); }; 24
        { par { 25
            { PHIL(1); }; 26
            { par { 27
                { PHIL(2); }; 28
                { par { 29
                    { PHIL(3); }; 30
                    { PHIL(4); } 31
                }; 32
            } 33
        }; 34
    } 35
}; 36
} 37
} 38
} 39
} 40
} 41
} 42
} 43
} 44

```

```

1 // -----
2 // Process ALL_FORK
3 // -----
4 inline void ALL_FORK (){
5     par{
6         { FORK(0); };
7         { par{
8             { FORK(1); };
9             { par{
10                 { FORK(2); };
11                 { par{
12                     { FORK(3); };
13                     { FORK(4); }
14                 };
15             }
16         };
17     }
18 };
19 }
20 }
21 }
22
23 // -----
24 // Process TABLE
25 // -----
26 inline void TABLE (){
27     par{
28         { ALL_PHIL(); };
29         { ALL_FORK(); }
30     }
31 }
32
33 // -----
34 // Process TABLE_P
35 // -----
36 inline void TABLE_P (){
37     par{ { ENVIRONMENT(); };
38         { TABLE(); } }
39 }
40
41 // -----
42 // Declaration of the main method
43 // -----
44 void main(){ TABLE_P(); }

```

6.2 Level Crossing

6.2.1 CSP_M

```
----- 1
-- LEVEL CROSSING -- 2
----- 3

--!! main (CARS ||| TRAINS ||| GATE) [| {|train,car,gate|} |] CONTROL_CAR 4
---- FOR FDR CHECK, UNCOMMENT LINE BELOW 5
--MAIN = (CARS ||| TRAINS ||| GATE) [| {|train,car,gate|} |] CONTROL_CAR 6
----- 7
----- 8
-- DATATYPES -- 9
----- 10
----- 11
----- 12
datatype LETTER = A | B | C 13
datatype CAR_REG = consulate | official | L.LETTER 14
datatype TRAIN_COMPANY = GNER | Virgin 15
datatype UD = raise | lower 16
datatype ELA = enter | leave | approach 17
----- 18
----- 19
-- CHANNELS -- 20
----- 21
----- 22
channel car:ELA.CAR_REG 23
channel train:ELA.TRAIN_COMPANY 24
channel gate:UD 25
----- 26
----- 27
-- CARS -- 28
----- 29
CARS = car.approach!L.A -> car.enter!L.A -> car.leave!L.A -> 30
      car.approach!L.B -> car.enter!L.B -> car.leave!L.B -> 31
      car.approach!L.C -> car.enter!L.C -> car.leave!L.C -> 32
      car.approach!consulate -> car.enter!consulate -> 33
      car.leave!consulate -> 34
      car.approach!official -> car.enter!official -> 35
      car.leave!official -> CARS 36
----- 37
----- 38
----- 39
----- 40
----- 41
```

```

1
2 -----
3 -- TRAINS --
4 -----
5 TRAINS = train.approach!GNER -> train.enter!GNER ->
6         train.leave!GNER -> train.approach!Virgin ->
7         train.enter!Virgin -> train.leave!Virgin -> TRAINS
8
9 -----
10 -- GATE --
11 -----
12 --!! channel gate out within GATE
13 GATE = gate.lower -> gate.raise -> GATE
14
15 -----
16 -- CONTROLLER - GATE IS UP --
17 -----
18 --!! channel gate in within CONTROL_CAR
19 CONTROL_CAR = car.approach?id -> car.enter?id ->
20               car.leave?id -> CONTROL_TRAIN
21
22 --!! channel gate in within CONTROL_TRAIN
23 CONTROL_TRAIN = train.approach?id -> gate.lower ->
24               train.enter?id -> train.leave?id ->
25               gate.raise -> CONTROL_CAR

```

6.2.2 Handel-C

```

26
27 // -----
28 // Declaration of the system's clock
29 // -----
30 set clock = external "clock1";
31
32
33 // -----
34 // Declaration of the empty channel set
35 // -----
36 #define channels_nil_s 0b0
37
38
39
40
41

```

```

// ----- 1
// Declaration of auxiliary constants for communication 2
// through synchronisation channels 3
// ----- 4
typedef unsigned int 1 SYNC; 5
const SYNC syncout =0; 6
SYNC syncin; 7

// ----- 8
// Declaration of constants related to the datatype boolean 9
// ----- 10
// ----- 11
// ----- 12
// Declaration of constants related to the datatype boolean 13
// ----- 14
// ----- 15
#define boolean unsigned int 1 16
#define false 0 17
#define true 1 18
#define boolean_card 2 19
#define boolean_set unsigned int boolean_card 20
#define true_s 0b10 21
#define false_s 0b01 22
#define boolean_nil_s 0b00 23
static boolean_set boolean_sets_LUT[boolean_card] ={ 24
    false_s,true_s}; 25

// ----- 26
// Declaration of constants related to the datatype integer 27
// ----- 28
// ----- 29
#define integer int 1 30
#define integer_card 2 31
#define integer_offset integer_card / 2 32
#define integer_set unsigned int integer_card 33
#define integer_neg_1_s 0b10 34
#define integer_0_s 0b01 35
#define integer_nil_s 0b00 36
static integer_set integer_sets_LUT[integer_card] ={ 37
    integer_0_s,integer_neg_1_s}; 38

// ----- 39
// Declaration of constants related to the datatype LETTER 40
// ----- 41
// ----- 42
#define LETTER unsigned int 2 43
#define C 0 44

```



```

1  #define B 1
2  #define A 2
3  #define LETTER_card 3
4  #define LETTER_set unsigned int LETTER_card
5  #define A_s 0b100
6  #define B_s 0b010
7  #define C_s 0b001
8  #define LETTER_nil_s 0b000
9  static LETTER_set LETTER_sets_LUT[LETTER_card] ={
10     C_s,B_s,A_s};
11
12  // -----
13  // Declaration of constants related to the datatype CAR_REG
14  // -----
15  #define CAR_REG unsigned int 3
16  #define L_A 0
17  #define L_B 1
18  #define L_C 2
19  #define official 3
20  #define consulate 4
21  #define CAR_REG_card 5
22  #define CAR_REG_set unsigned int CAR_REG_card
23  #define consulate_s 0b10000
24  #define official_s 0b01000
25  #define L_C_s 0b00100
26  #define L_B_s 0b00010
27  #define L_A_s 0b00001
28  #define CAR_REG_nil_s 0b00000
29  static CAR_REG_set CAR_REG_sets_LUT[CAR_REG_card] ={
30     L_A_s,L_B_s,L_C_s,official_s,consulate_s};
31  static CAR_REG CAR_REG_L_LUT[LETTER_card] ={
32     L_C ,L_B ,L_A};
33
34  // -----
35  // Declaration of constants related to the datatype TRAIN_COMPANY
36  // -----
37  #define TRAIN_COMPANY unsigned int 1
38  #define Virgin 0
39  #define GNER 1
40  #define TRAIN_COMPANY_card 2
41  #define TRAIN_COMPANY_set unsigned int TRAIN_COMPANY_card
42  #define GNER_s 0b10
43  #define Virgin_s 0b01
44  #define TRAIN_COMPANY_nil_s 0b00

```

```

static TRAIN_COMPANY_set TRAIN_COMPANY_sets_LUT[TRAIN_COMPANY_card] ={
    Virgin_s,GNER_s};

// -----
// Declaration of constants related to the datatype UD
// -----
#define UD unsigned int 1
#define lower 0
#define raise 1
#define UD_card 2
#define UD_set unsigned int UD_card
#define raise_s 0b10
#define lower_s 0b01
#define UD_nil_s 0b00
static UD_set UD_sets_LUT[UD_card] ={
    lower_s,raise_s};

// -----
// Declaration of constants related to the datatype ELA
// -----
#define ELA unsigned int 2
#define approach 0
#define leave 1
#define enter 2
#define ELA_card 3
#define ELA_set unsigned int ELA_card
#define enter_s 0b100
#define leave_s 0b010
#define approach_s 0b001
#define ELA_nil_s 0b000
static ELA_set ELA_sets_LUT[ELA_card] ={
    approach_s,leave_s,enter_s};

// -----
// Constants Prototypes
// -----

// -----
// Constants
// -----

```

1
2
3
4
5
6
7
8
9
10
11
12
13
14
15
16
17
18
19
20
21
22
23
24
25
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44

```

1 // -----
2 // Declaration of the channels
3 // -----
4 chan SYNC gate[UD_card];
5 chan TRAIN_COMPANY train[ELA_card];
6 chan CAR_REG car[ELA_card];
7
8 // -----
9 // Declaration of constants for each of the processes in the mutual recursion
10 // -----
11 #define GATE 4
12 #define CONTROL_CAR 3
13 #define TRAINS 2
14 #define CONTROL_TRAIN 1
15 #define CARS 0
16
17 // -----
18 // Declaration of the local copies of each the processes arguments
19 // -----
20
21 // -----
22 // Declaration of method corresponding to the mutual recursion
23 // -----
24 inline void MUTUAL_REC(int 3 PROGRAM_COUNTER){
25     boolean KEEP_LOOPING;
26     KEEP_LOOPING = true;
27     while(KEEP_LOOPING){
28         KEEP_LOOPING = false;
29         switch(PROGRAM_COUNTER){
30             // -----
31             // Process CARS
32             // -----
33             case CARS :{
34                 seq{
35                     car[approach]! CAR_REG_L_LUT[A];
36                     seq{
37                         car[enter]! CAR_REG_L_LUT[A];
38                         seq{
39                             car[leave]! CAR_REG_L_LUT[A];
40                             seq{
41                                 car[approach]! CAR_REG_L_LUT[B];
42                                 seq{
43                                     car[enter]! CAR_REG_L_LUT[B];

```

seq{	1
car[leave]! CAR_REG_L_LUT[B];	2
seq{	3
car[approach]! CAR_REG_L_LUT[C];	4
seq{	5
car[enter]! CAR_REG_L_LUT[C];	6
seq{	7
car[leave]! CAR_REG_L_LUT[C];	8
seq{	9
car[approach]! consulate;	10
seq{	11
car[enter]! consulate;	12
seq{	13
car[leave]! consulate;	14
seq{	15
car[approach]! official;	16
seq{	17
car[enter]! official;	18
seq{	19
car[leave]! official;	20
PROGRAM_COUNTER = CARS;	21
KEEP_LOOPING = true;	22
};	23
};	24
};	25
};	26
};	27
};	28
};	29
};	30
};	31
};	32
};	33
};	34
};	35
};	36
}	37
break;	38
}	39
	40
	41
	42
	43
	44

```

1
2 // -----
3 // Process CONTROL_TRAIN
4 // -----
5 case CONTROL_TRAIN :{
6     seq{
7         TRAIN_COMPANY id;
8         train[approach]? id;
9         seq{
10             gate[lower]?syncin;
11             seq{
12                 TRAIN_COMPANY id;
13                 train[enter]? id;
14                 seq{
15                     TRAIN_COMPANY id;
16                     train[leave]? id;
17                     seq{
18                         gate[raise]?syncin;
19                         PROGRAM_COUNTER = CONTROL_CAR;
20                         KEEP_LOOPING = true;
21                     };
22                 };
23             };
24         };
25     }
26     break;
27 }
28
29 // -----
30 // Process TRAINS
31 // -----
32 case TRAINS :{
33     seq{
34         train[approach]! GNER;
35         seq{
36             train[enter]! GNER;
37             seq{
38                 train[leave]! GNER;
39                 seq{
40                     train[approach]! Virgin;
41                     seq{
42                         train[enter]! Virgin;
43                         seq{

```

train[leave]! Virgin;	1
PROGRAM_COUNTER = TRAINS;	2
KEEP_LOOPING = true;	3
};	4
};	5
};	6
};	7
};	8
}	9
break;	10
}	11
	12
// -----	13
// Process CONTROL_CAR	14
// -----	15
case CONTROL_CAR :{	16
seq{ CAR_REG id;	17
car[approach]? id;	18
seq{ CAR_REG id;	19
car[enter]? id;	20
seq{ CAR_REG id;	21
car[leave]? id;	22
PROGRAM_COUNTER = CONTROL_TRAIN;	23
KEEP_LOOPING = true;	24
};	25
};	26
}	27
break;	28
}	29
	30
// -----	31
// Process GATE	32
// -----	33
case GATE :{	34
seq{	35
gate[lower]!syncout;	36
seq{	37
gate[raise]!syncout;	38
PROGRAM_COUNTER = GATE;	39
KEEP_LOOPING = true;	40
};	41
}	42
break;	43
}	44

```

1         default: KEEP_LOOPING = false;
2     }
3 }
4 }
5
6 // -----
7 // Declaration of the main method
8 // -----
9 void main(){
10     par{
11         {
12             par{
13                 { MUTUAL_REC(CARS); };
14                 {
15                     par{
16                         { MUTUAL_REC(TRAINS); };
17                         { MUTUAL_REC(GATE); }
18                     };
19                 }
20             };
21         };
22         { MUTUAL_REC(CONTROL_CAR); }
23     }
24 }

```

6.3 CMOS Phase Controller

6.3.1 CSP_M

```

25 -----
26 -- CMOS PHASE CONTROLLER --
27 -----
28
29 -----
30 -- CMOS PHASE CONTROLLER PROTOTYPE --
31 -----
32 --!! main PHASE_COORD_INIT [| {| phase |} |] ENVIRONMENT
33 ---- FOR FDR CHECK, UNCOMMENT LINE BELOW
34 --MAIN = PHASE_COORD_INIT [| {| phase |} |] ENVIRONMENT
35
36 -----
37 -- DATATYPES --
38 -----
39

```

```

datatype PhaseMode = start | elaborate | perform | iterate | stop
1
2
datatype Phase = init_p | env_input_p | propose_p | decide_p
3
               | apply_p | env_output_p
4
5
-----
6
-- CHANNELS --
7
-----
8
9
channel phase:Phase.PhaseMode
10
11
-----
12
-- INIT PHASE --
13
-----
14
--!! channel phase out within PHASE_COORD_INIT
15
PHASE_COORD_INIT = phase.init_p.start ->
16
                  phase.init_p.stop -> PHASE_COORD_INPUT
17
18
-----
19
-- INPUT PHASE --
20
-----
21
--!! channel phase out within PHASE_COORD_INPUT
22
PHASE_COORD_INPUT = phase.env_input_p.start ->
23
                  phase.env_input_p.stop -> PHASE_COORD_PROPOSE
24
25
-----
26
-- PROPOSE PHASE --
27
-----
28
--!! channel phase out within PHASE_COORD_PROPOSE
29
PHASE_COORD_PROPOSE = phase.propose_p.start ->
30
                    phase.propose_p.elaborate ->
31
                    phase.propose_p.perform ->
32
                    phase.propose_p.stop ->
33
                    PHASE_COORD_DECIDE
34
35
-----
36
-- DECIDE PHASE --
37
-----
38
--!! channel phase out within PHASE_COORD_DECIDE
39
PHASE_COORD_DECIDE = phase.decide_p.start ->
40
                    phase.decide_p.stop -> PHASE_COORD_APPLY
41
42
43
44

```



```

1
2
3 -----
4 -- APPLY PHASE --
5 -----
6 --!! channel phase out within PHASE_COORD_APPLY
7 PHASE_COORD_APPLY = phase.apply_p.start -> PHASE_COORD_APPLY_LOOP
8
9 --!! channel phase out within PHASE_COORD_APPLY_LOOP
10 PHASE_COORD_APPLY_LOOP = phase.apply_p.elaborate ->
11     phase.apply_p.perform -> (
12         phase.apply_p.iterate -> PHASE_COORD_APPLY_LOOP
13         []
14         phase.apply_p.stop -> PHASE_COORD_OUTPUT
15     )
16
17 -----
18 -- OUTPUT PHASE --
19 -----
20 --!! channel phase out within PHASE_COORD_OUTPUT
21 PHASE_COORD_OUTPUT = phase.env_output_p.start ->
22     phase.env_output_p.stop -> PHASE_COORD_INPUT
23
24 -----
25 -- ENVIRONMENT --
26 -----
27 --!! channel phase in within ENVIRONMENT
28 ENVIRONMENT = phase.init_p.start ->
29     phase.init_p.stop -> ENVIRONMENT_INPUT
30
31 --!! channel phase in within ENVIRONMENT_INPUT
32 ENVIRONMENT_INPUT =
33     phase.env_input_p.start -> phase.env_input_p.stop ->
34     phase.propose_p.start -> phase.propose_p.elaborate ->
35     phase.propose_p.perform -> phase.propose_p.stop ->
36     phase.decide_p.start -> phase.decide_p.stop ->
37     phase.apply_p.start -> phase.apply_p.elaborate ->
38     phase.apply_p.perform -> phase.apply_p.iterate ->
39     phase.apply_p.elaborate ->
40     phase.apply_p.perform ->
41     phase.apply_p.stop ->
42     phase.env_output_p.start ->
43     phase.env_output_p.stop ->
44     ENVIRONMENT_INPUT

```

6.3.2 Handel-C

```
// -----
// Declaration of the system's clock
// -----
set clock = external "clock1";

// -----
// Declaration of the empty channel set
// -----
#define channels_nil_s 0b0

// -----
// Declaration of auxiliary constants for
// communication through synchronisation channels
// -----
typedef unsigned int 1 SYNC;
const SYNC syncout =0;
SYNC syncin;

// -----
// Declaration of constants related to the datatype boolean
// -----

// -----
// Declaration of constants related to the datatype boolean
// -----
#define boolean unsigned int 1
#define false 0
#define true 1
#define boolean_card 2
#define boolean_set unsigned int boolean_card
#define true_s 0b10
#define false_s 0b01
#define boolean_nil_s 0b00
static boolean_set boolean_sets_LUT[boolean_card] ={
    false_s,true_s};

// -----
```

```

1 // Declaration of constants related to the datatype integer
2 // -----
3 #define integer_int 1
4 #define integer_card 2
5 #define integer_offset integer_card / 2
6 #define integer_set unsigned int integer_card
7 #define integer_neg_1_s 0b10
8 #define integer_0_s 0b01
9 #define integer_nil_s 0b00
10 static integer_set integer_sets_LUT[integer_card] ={
11     integer_0_s,integer_neg_1_s};
12
13 // -----
14 // Declaration of constants related to the datatype PhaseMode
15 // -----
16 #define PhaseMode unsigned int 3
17 #define stop 0
18 #define iterate 1
19 #define perform 2
20 #define elaborate 3
21 #define start 4
22 #define PhaseMode_card 5
23 #define PhaseMode_set unsigned int PhaseMode_card
24 #define start_s 0b10000
25 #define elaborate_s 0b01000
26 #define perform_s 0b00100
27 #define iterate_s 0b00010
28 #define stop_s 0b00001
29 #define PhaseMode_nil_s 0b00000
30 static PhaseMode_set PhaseMode_sets_LUT[PhaseMode_card] ={
31     stop_s,iterate_s,perform_s,elaborate_s,start_s};
32
33 // -----
34 // Declaration of constants related to the datatype Phase
35 // -----
36 #define Phase unsigned int 3
37 #define env_output_p 0
38 #define apply_p 1
39 #define decide_p 2
40 #define propose_p 3
41 #define env_input_p 4
42 #define init_p 5
43 #define Phase_card 6

```

```

#define Phase_set unsigned int Phase_card 1
#define init_p_s 0b100000 2
#define env_input_p_s 0b010000 3
#define propose_p_s 0b001000 4
#define decide_p_s 0b000100 5
#define apply_p_s 0b000010 6
#define env_output_p_s 0b000001 7
#define Phase_nil_s 0b000000 8
static Phase_set Phase_sets_LUT[Phase_card] ={ 9
    env_output_p_s,apply_p_s,decide_p_s,propose_p_s,env_input_p_s,init_p_s};10
11
// -----12
// Constants Prototypes13
// -----14
15
// -----16
// Constants17
// -----18
19
// -----20
// Declaration of the channels21
// -----22
chan SYNC phase[Phase_card][PhaseMode_card];23
24
// -----25
// Declaration of constants for each of the processes in the mutual recursion26
// -----27
#define ENVIRONMENT 828
#define PHASE_COORD_DECIDE 729
#define PHASE_COORD_PROPOSE 630
#define PHASE_COORD_OUTPUT 531
#define ENVIRONMENT_INPUT 432
#define PHASE_COORD_INIT 333
#define PHASE_COORD_APPLY 234
#define PHASE_COORD_INPUT 135
#define PHASE_COORD_APPLY_LOOP 036
37
// -----38
// Declaration of the local copies of each the processes arguments39
// -----40
41
// -----42
// Declaration of method corresponding to the mutual recursion43
// -----44

```

```

1 inline void MUTUAL_REC(int 4 PROGRAM_COUNTER){
2     boolean KEEP_LOOPING;
3     KEEP_LOOPING = true;
4     while(KEEP_LOOPING){
5         KEEP_LOOPING = false;
6         switch(PROGRAM_COUNTER){
7
8             // -----
9             // Process PHASE_COORD_APPLY_LOOP
10            // -----
11            case PHASE_COORD_APPLY_LOOP :{
12                seq{
13                    phase[apply_p][elaborate]!syncout;
14                    seq{
15                        phase[apply_p][perform]!syncout;
16                        prialt{
17                            case phase[apply_p][iterate]!syncout :{
18                                PROGRAM_COUNTER = PHASE_COORD_APPLY_LOOP;
19                                KEEP_LOOPING = true;
20                            };
21                            break;
22                            case phase[apply_p][stop]!syncout :{
23                                PROGRAM_COUNTER = PHASE_COORD_OUTPUT;
24                                KEEP_LOOPING = true;
25                            };
26                            break;
27                        };
28                    };
29                }
30                break;
31            }
32
33            // -----
34            // Process PHASE_COORD_INPUT
35            // -----
36            case PHASE_COORD_INPUT :{
37                seq{ phase[env_input_p][start]!syncout;
38                    seq{ phase[env_input_p][stop]!syncout;
39                        PROGRAM_COUNTER = PHASE_COORD_PROPOSE;
40                        K EEP_LOOPING = true;
41                    };
42                }
43                break;
44            }

```

	1
	2
	3
	4
// -----	5
// Process PHASE_COORD_APPLY	6
// -----	7
case PHASE_COORD_APPLY :{	8
seq{	9
phase[apply_p][start]!syncout;	10
PROGRAM_COUNTER = PHASE_COORD_APPLY_LOOP;	11
KEEP_LOOPING = true;	12
}	13
break;	14
}	15
	16
// -----	17
// Process PHASE_COORD_INIT	18
// -----	19
case PHASE_COORD_INIT :{	20
seq{	21
phase[init_p][start]!syncout;	22
seq{	23
phase[init_p][stop]!syncout;	24
PROGRAM_COUNTER = PHASE_COORD_INPUT;	25
KEEP_LOOPING = true;	26
};	27
}	28
break;	29
}	30
	31
// -----	32
// Process ENVIRONMENT_INPUT	33
// -----	34
case ENVIRONMENT_INPUT :{	35
seq{	36
phase[env_input_p][start]?syncin;	37
seq{	38
phase[env_input_p][stop]?syncin;	39
seq{	40
phase[propose_p][start]?syncin;	41
seq{	42
phase[propose_p][elaborate]?syncin;	43

```

1      seq{
2      phase[propose_p] [perform]?syncin;
3      seq{
4      phase[propose_p] [stop]?syncin;
5      seq{
6          phase[decide_p] [start]?syncin;
7          seq{
8          phase[decide_p] [stop]?syncin;
9          seq{
10             phase[apply_p] [start]?syncin;
11             seq{
12             phase[apply_p] [elaborate]?syncin;
13             seq{
14             phase[apply_p] [perform]?syncin;
15             seq{
16             phase[apply_p] [iterate]?syncin;
17             seq{
18                 phase[apply_p] [elaborate]?syncin;
19                 seq{
20                 phase[apply_p] [perform]?syncin;
21                 seq{
22                 phase[apply_p] [stop]?syncin;
23                 seq{
24                     phase[env_output_p] [start]?syncin;
25                     seq{
26                         phase[env_output_p] [stop]?syncin;
27                         PROGRAM_COUNTER = ENVIRONMENT_INPUT;
28                         KEEP_LOOPING = true;
29                     };
30                 };
31             };
32             };
33         };
34         };
35         };
36         };
37     };
38 };
39 };
40 };
41 };
42 };
43 };
44 };

```

}	1
break;	2
}	3
	4
	5
// -----	6
// Process PHASE_COORD_OUTPUT	7
// -----	8
case PHASE_COORD_OUTPUT :{	9
seq{	10
phase[env_output_p] [start] !syncout;	11
seq{	12
phase[env_output_p] [stop] !syncout;	13
PROGRAM_COUNTER = PHASE_COORD_INPUT;	14
KEEP_LOOPING = true;	15
};	16
}	17
break;	18
}	19
	20
// -----	21
// Process PHASE_COORD_PROPOSE	22
// -----	23
case PHASE_COORD_PROPOSE :{	24
seq{	25
phase[propose_p] [start] !syncout;	26
seq{	27
phase[propose_p] [elaborate] !syncout;	28
seq{	29
phase[propose_p] [perform] !syncout;	30
seq{	31
phase[propose_p] [stop] !syncout;	32
PROGRAM_COUNTER = PHASE_COORD_DECIDE;	33
KEEP_LOOPING = true;	34
};	35
};	36
};	37
}	38
break;	39
}	40
	41
	42
	43
	44


```

1
2
3
4
5
6         // -----
7         // Process PHASE_COORD_DECIDE
8         // -----
9         case PHASE_COORD_DECIDE :{
10            seq{
11                phase[decide_p][start]!syncout;
12                seq{
13                    phase[decide_p][stop]!syncout;
14                    PROGRAM_COUNTER = PHASE_COORD_APPLY;
15                    KEEP_LOOPING = true;
16                };
17            }
18            break;
19        }
20
21        // -----
22        // Process ENVIRONMENT
23        // -----
24        case ENVIRONMENT :{
25            seq{
26                phase[init_p][start]?syncin;
27                seq{
28                    phase[init_p][stop]?syncin;
29                    PROGRAM_COUNTER = ENVIRONMENT_INPUT;
30                    KEEP_LOOPING = true;
31                };
32            }
33            break;
34        }
35        default: KEEP_LOOPING = false;
36    }
37 }
38 }
39
40 // -----
41 // Declaration of the main method
42 // -----
43 void main(){

```

par{	1
{ MUTUAL_REC(PHASE_COORD_INIT); };	2
{ MUTUAL_REC(ENVIRONMENT); }	3
}	4
}	5